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By Shih-Chii Liu;Jorg Kramer;Giacomo Indiveri

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This particular circuit is designed in what is known as an alternating topology, and field-programmable analog arrays (FPAAs). References

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Shih-Chii Liu, Tobi Delbruck, Giacomo Indiveri, Liu, S-C. and Kramer, J. and Indiveri, R. Analog VLSI: Circuits and Principles, MIT Press, 2002 2001.

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